EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	77	10 and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:44
L10	77	8 not 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:43
L9	26	(US-20060236274-$ or US-20040133833-$ or US-20020173942-$).did. or (US-7051303-$ or US-6785873-$ or US-6591403-$ or US-6539523-$ or US-6493852-$ or US-6389379-$ or US-6108494-$ or US-6026230-$ or US-6009256-$ or US-6845489-$ or US-6510541-$ or US-6088821-$ or US-6212489-$ or US-6496972-$ or US-6978431-$ or US-6574778-$ or US-6374205-$ or US-6345378-$ or US-	US-PGPUB; USPAT	OR	ON	2008/05/07 15:41

		7103863-$ or US-6421808-$ or US-5748486-$ or US-5515526-$ or US-7206731-$).did.				
L8	78	1 2 3 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:39
L7	0	((simulat$3 emulat$3) WITH (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same (((reconstruct reconstructed reconstructing reconstruction re $1construct re $1constructed re $1constructing re $1construction reassemble reassembled reassembling re $1assemble re $1assembled re $1assembling restore restored restoring restoration re$1store re$1stored re $1storing re $1storation) SAME (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) SAME (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:35

		component subcomponent sub $1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))				
L6	0	((simulat$3 emulat$3) WITH (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same (((reconstruct reconstructed reconstructing reconstruction re $1construct re $1constructed re $1constructing re $1construction reassemble reassembled reassembling re $1assemble re $1assembled re $1assembling restore restored restoring restoration re$1store re$1stored re $1storing re $1storation) WITH (NETLIST NET$1LIST	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:28

		RTL REGISTER $1TRANSFER $1LEVEL CIRCUIT ckt MICRO $1CIRCUIT ASIC MODEL logic)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))				
L5	0	((simulat$3 emulat$3) WITH (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((reconstruct reconstructed reconstructing reconstruction re $1construct re $1constructed re $1constructing re $1construction reassemble reassembled reassembling re $1assemble re $1assembled re $1assembling restore restored restoring restoration re$1store re$1stored re	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:24

$1storing re $1storation) WITH (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))

L4	68	((simulat$3 emulat$3) WITH (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) SAME (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) SAME (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:13

		troubleshooting trouble$1shoot trouble$1shooted trouble$1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co$1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co$1verify$3 (co adj validat$3) co$1validat$3))				
L3	37	((simulat$3 emulat$3) WITH (verifi$6 verify$3 validat$3 test$3 "logic checking" logic$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) WITH (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:11

		subcomponent sub $1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))				
L2	44	((simulat$3 emulat$3) WITH (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising) WITH (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:03

		sub$1module component subcomponent sub$1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble$1shoot trouble$1shooted trouble$1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co$1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co$1verify$3 (co adj validat$3) co$1validat$3))					
L1	66	((simulat$3 emulat$3) WITH (verifi$6 verify$3 validat$3 test$3 "logic checking" logic$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub$1component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 14:51

		and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3))					
S63	26	S61 and (bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/01 17:01
S62	21	S61 and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble $1shoot trouble $1shooted trouble $1shooting) WITH (simulat$3 emulat$3 (co adj simulat$3) co $1simulat$3 (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/01 15:13

		validat$3) co$1validat $3))				
S61	26	(US-20060236274-$ or US-20040133833-$ or US-20020173942-$).did. or (US-7051303-$ or US-6785873-$ or US-6591403-$ or US-6539523-$ or US-6493852-$ or US-6389379-$ or US-6108494-$ or US-6026230-$ or US-6009256-$ or US-6845489-$ or US-6510541-$ or US-6088821-$ or US-6212489-$ or US-6496972-$ or US-6978431-$ or US-6574778-$ or US-6374205-$ or US-6345378-$ or US-7103863-$ or US-6421808-$ or US-5748486-$ or US-5515526-$ or US-7206731-$).did.	US-PGPUB; USPAT	OR	ON	2008/05/01 15:13
S60	26	S49 and (verifi$6 verify $3 validat$3 test$3 "logic checking" logic $1checking (co adj verifi$6) co$1verifi$6 (co adj verify$3) co $1verify$3 (co adj validat$3) co$1validat $3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 21:52
S59	25	S49 and simulat$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 21:45

S56	1	S49 and ((reconstruct reconstructed reconstructing reconstruction re$1construct re$1constructed re$1constructing re$1construction reassemble reassembled reassembling re$1assemble re$1assembled re$1assembling restore restored restoring restoration re$1store re$1stored re$1storing re$1storation) SAME (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor$3) NEAR data)) SAME (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub$1component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 18:00
S55	6	S49 and ((reconstruct reconstructed reconstructing reconstruction re$1construct re$1constructed re$1constructing re$1construction reassemble reassembled reassembling re$1assemble re$1assembled re$1assembling restore restored restoring restoration re$1store re$1stored re$1storing re	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 18:00

		$1storation) WITH (NETLIST NET$1LIST RTL REGISTER $1TRANSFER $1LEVEL CIRCUIT ckt MICRO $1CIRCUIT ASIC MODEL logic))				
S54	0	S49 and ((reconstruct reconstructed reconstructing reconstruction re $1construct re $1constructed re $1constructing re $1construction reassemble reassembled reassembling re $1assemble re $1assembled re $1assembling restore restored restoring restoration re$1store re$1stored re $1storing re $1storation) WITH (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 17:58

| S53 | 19 | S49 and ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) SAME (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) SAME (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/30 15:32 |
| S52 | 12 | S49 and ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) WITH (list listed listing enumerate enumerated enumeration enumerating database | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/30 13:52 |

		data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model))					
S51	3	S49 and ((optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising) WITH (list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component subcomponent sub $1component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 13:51
S50	24	S49 and ((list listed listing enumerate enumerated enumeration enumerating database data$1base databank data$1bank (data adj repository) ((sav$3 stor $3) NEAR data)) WITH (node net wire gate logic cell block subblock sub$1block circuit sub$1circuit subcircuit object module submodule sub$1module component	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 13:49

		subcomponent sub $1component model))				
S49	26	(US-20060236274-$ or US-20040133833-$ or US-20020173942-$).did. or (US-7051303-$ or US-6785873-$ or US-6591403-$ or US-6539523-$ or US-6493852-$ or US-6389379-$ or US-6108494-$ or US-6026230-$ or US-6009256-$ or US-6845489-$ or US-6510541-$ or US-6088821-$ or US-6212489-$ or US-6496972-$ or US-6978431-$ or US-6574778-$ or US-6374205-$ or US-6345378-$ or US-7103863-$ or US-6421808-$ or US-5748486-$ or US-5515526-$ or US-7206731-$).did.	US-PGPUB; USPAT	OR	ON	2008/04/30 13:49

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